



Sheet 1 of 2

<b>Form PTO-1449 Modified</b>  List of Patent and Publications Cited by Applicant (Use several sheets if necessary)  U.S. Department of Commerce Patent and Trademark Office	Docket No. <b>MSFT-2522/304105.02</b>	Application No. <b>09/918,295</b>
	Applicant <b>Eric P. Traut, Aaron Giles and Parag Chakraborty</b>	
	Filing Date <b>July 30, 2001</b>	Group <b>2177</b>
	Confirmation No. <b>9879</b>	
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>		
<b>LW</b>	<b>1</b>	<b>"Bootable Hard Drive Emulation for Windows Clients", 3Com Virtual Lan Drive Data Sheet, February 2001, 1-4</b>
<b>EXAMINER</b> <i>Leslie Wong</i>		<b>DATE CONSIDERED</b> <i>6/10/2005</i>

**RECEIVED**

MAR 26 2004

Technology Center 2100



Sheet 2 of 2

<b>Form PTO-1449 Modified</b>  List of Patent and Publications Cited by Applicant (Use several sheets if necessary)  U.S. Department of Commerce Patent and Trademark Office				Docket No. <b>MSFT-2522/304105.02</b>		Application No. <b>09/918,295</b>	
				Applicant <b>Eric P. Traut, Aaron Giles and Parag Chakraborty</b>			
				Filing Date <b>July 30, 2001</b>		Group <b>2177</b>	
				Confirmation No. <b>9879</b>			
<b>U. S. PATENT DOCUMENTS</b>							
Examiner Initial		Document No.	Date	Name	Class	Subclass	
LW	2	6,014,651	01/11/00	Crawford	705	400	
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial		Document No.	Date	Country	Translation		
					YES	NO	
LW	3	WO 99/57641	11/11/99	PCT			
LW	4	WO 00/63801	10/26/00	PCT			
EXAMINER <i>Leslie Wong</i>				DATE CONSIDERED <i>6/10/2005</i>			

RECEIVED

MAR 26 2004

Technology Center 2100

PTO-1449		Application No. 09/918,295		Applicant(s) Traut, et al.	
Information Disclosure Citation in an Application		Docket Number 068167.0108		Group Art Unit 2181	Filing Date 7/30/01

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
LW	A	5278973	1/11/94	O'Brien et al.	395	500	6/27/91
	B	5301277	4/5/94	Kanai	709	301	12/23/91
	C	5367628	11/22/94	Ote et al.	345	501	10/15/92
	D	5448264	9/5/95	Pinedo et al.	345	508	6/14/93
	E	5452456	9/19/95	Mourey et al.	713	100	12/18/92
	F	5502809	3/26/96	Takano	345	509	9/6/94
	G	5640562	6/17/97	Wold et al.	395	652	2/27/95
	H	5666521	9/9/97	Marisetty	345	525	11/20/96
	I	5742797	4/21/98	Celi, Jr. et al.	345	507	8/11/95
	J	5752275	5/12/98	Hammond	711	207	7/14/97
	K	5757386	5/26/98	Celi, Jr. et al.	345	507	8/11/95
	L	5790825	8/4/98	Traut	712	209	8/5/97
	M	5831607	11/3/98	Brooks	345	333	1/25/96
	N	5860147	1/12/99	Gochman et al.	711	207	9/16/96
	O	5940872	8/17/99	Hammond et al.	711	207	11/1/96
	P	6014170	1/11/00	Pont et al.	348	232	6/20/97
	Q	6026476	2/15/00	Rosen	711	206	8/6/97
LW	R	6067618	5/23/00	Weber	713	1	3/26/98

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO

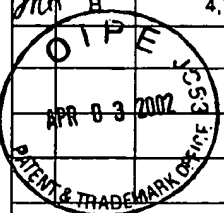
NON-PATENT DOCUMENTS		
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)
LW	S	"Processor Instruction Sets," The PC Guide, version date 12/18/00, <a href="http://www.pcguide.com/ref/cpu/arch/Int/Inst-c.html">http://www.pcguide.com/ref/cpu/arch/Int/Inst-c.html</a> .
I	T	"M68060 User's Manual," Motorola, 1994, pp. i-xviii; Section 4, Memory Management Unit, pp. 4-1 to 4-30, <a href="http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf">http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf</a> .
LW	U	"MPC750, RISC Microprocessor User's Manual," Motorola, 8/97, Contents, pp. iii-xvi; Chapter 5, Memory Management, pp. 5-1 to 5-34; Glossary, pp. Glossary-1 to Glossary-13, <a href="http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf">http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf</a> .

EXAMINER <i>Leslie Wong</i>	DATE CONSIDERED <i>6/10/2005</i>
--------------------------------	-------------------------------------

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

PTO-1449		Application No. 09/918,295		Applicant(s) Traut, et al.											
Information Disclosure Citation in an Application		Docket Number 068167.0108		Group Art Unit 2181	Filing Date 7/30/01										
U.S. PATENT DOCUMENTS															
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE								
JLH	A	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96								
JLH	B	4,779,188	10/18/88	Gum et al.	364	200	10/19/87								
															
								RECEIVED							
								APR 05 2002							
Technology Center 2100															
FOREIGN PATENT DOCUMENTS															
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION								
							YES	NO							
LW	C	EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X								
NON-PATENT DOCUMENTS															
DOCUMENT (Including Author, Title, Source, and Pertinent Pages)															
JRH	D	PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.													
	E	Traut E, "Building the Virtual PC," Byte, McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.													
	F	"Intel386 DX Microprocessor," Intel, pp. 32-58, 31 December 1995.													
	G	"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL: www.btech.co/changingchips.html, paragraphs '0006!-'0007!, retrieved 12/10/01.													
	H	"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.													
	I	Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," IBM Systems Journal, Vol. 30, No. 1, pp. 34-51, 1991.													
	J	Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," Microprocessing and Microprogramming, Vol. 22, No. 2, pp. 81-96, February 1988.													
EXAMINER					DATE CONSIDERED										
Jean R Homere					12/23/03										
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.															

U.S. Patent and Trademark Office

HOU03:838721

PTO-1449		APR 09 2002		Application No. 09/918,295		Applicant(s) Traut et al	
Information Disclosure Citation in this Application				Docket Number 0681,67.0108		Group Art Unit 2181	
						Filing Date 07/30/2001	
U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					
	A	U.S. Patent Application Serial No. 09/617,709, "System and Method for Emulating the Operation of a Translation Look-Aside Buffer", Traut, filed July 17, 2000					
	B	U.S. Patent Application Serial No. 09/617,669, "System and Method for Displaying Current Images of Virtual Machine Environments", Traut, et al., filed July 17, 2000					
	C	U.S. Patent Application Serial No. 09/617,624, "System and Method for Emulating the Operation of a Video Graphics Adapter", Carroll, et al., filed July 17, 2000					
	D	U.S. Patent Application Serial No. 09/747,492, "System and Method for the Logical Substitution of Processor Control in an Emulated Computing Environment", Traut, filed December 21, 2000					
	E	U.S. Patent Application Serial No. 09/809,731, "Method for Hybrid Processing of Software Instructions of an Emulated Computer System", Giles et al., filed March 15, 2001					
	F	U.S. Patent Application Serial No. 09/906,392, "System and Method for the Logical Substitution of Processor Control in an Emulated Computing Environment", Traut, filed July 16, 2001					
EXAMINER					DATE CONSIDERED		
Jean R Homer					12/23/03		
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							

U.S. Patent and Trademark Office

HOU03:840305.1

Filing Date  
7/30/01

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

## NON-PATENT DOCUMENTS

		<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>	
LW	E	PCT International Search Report in International Application No. PCT/US 01/22277, International filing date 7/16/01, mail date 2/7/02.	

leslie wong

6/10/2005

HOU03:833556